B.Sc. (Data Science)
Core Course (CC)
Semester IV
BSDB32401P: Computer System Architecture Lab

Total Marks: 100
External Marks: 70
Internal Marks: 30
Credits: 2
Pass Percentage: 40%

 Besides below given assignment List of Lab Assignments – Session wise will be given to student

1) To study basic gates and verify their truth tables.

2) To design and implement encoder and decoder.

3) To design and implement multiplexer.

4) To design and implement de-multiplexer.

5) To realize Basic gates (AND,OR, NOT) From Universal Gates(NAND & NOR).

6) To study about full adder & verify its truth tab

7) To Design Arithmetic Circuit

8) To test for Shift Circuit

9) To implement addressing modes.

10) To implement instruction cycle.